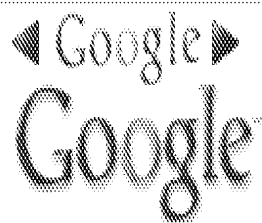


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1. Computer **network** with modified host-to-host **encryption** keys - US ...

The BQI value 144 in each **packet** header specifies which of the **buffer** queues 118 in the ... Using the DES CBC (**cipher**-block-chaining) **encryption** scheme, ... 222 and then temporarily stored in a **FIFO (first in first out) buffer** 224. ... The **first** step (A) of this process is to **decrypt** the encrypted key 146 in the ...

<https://www.patentstorm.us/patents/5268962/description.html> - [Similar pages](#)

2. High performance IPSEC hardware accelerator for **packet** ...

Feb 21, 2006 ... 6327625, **FIFO**-based **network** interface supporting **out**-of-order .... IPSEC **encryption** and **decryption** work within the IP layer of the **network** .... program the **cipher** engine to perform the desired **authentication** algorithm. ... is a **pointer** to the start of the outbound **packet** stored in a **buffer** (the ...

[www.freepatentsonline.com/7003118.html](http://www.freepatentsonline.com/7003118.html) - [Similar pages](#)

by LJ Yang - 2006 - [Related articles](#) - [All 3 versions](#)

3. Scalable key agile cryptography - Patent 5796836

wherein the keystream memory module comprises a **first** keystream **FIFO** memory, .... To **decrypt**, **cipher** text block 14 is processed by codebook 16 and the ... To select a key, the virtual channel address is used to select a **pointer** to the .... **encryption** and **decryption** for the devices they connect to ATM **network** 108. ...

[www.freepatentsonline.com/5796836.html](http://www.freepatentsonline.com/5796836.html) - [Similar pages](#)

by TR Markham - 1998 - [Cited by 5](#) - [Related articles](#) - [All 3 versions](#)

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4. Data Flow Control Within and Between DMA Channels - Patents

The loopback circuit 40 may supply a **buffer pointer** from the DMA descriptor for

the ..... Similarly, the selected **encryption/decryption (cipher)** algorithm, ... Finally, the **network** layer encapsulates the IP **packet** with an Ethernet ... The **buffer** 232 may be the source **buffer** for a **first** DMA transfer (DMA 1 in FIG. ...

[www.faqs.org/patents/app/20080222317](http://www.faqs.org/patents/app/20080222317) - 124k - [Cached](#) - [Similar pages](#)

#### 5. (WO/1996/033564) METHOD AND APPARATUS FOR HIGH SPEED BLOCK ...

To **decrypt, cipher** text block 14 is processed by codebook 16 and the output ... The Stevenson design must **encrypt** the current **packet** fast enough that a backlog .... in effect, a **First In First Out (FIFO)** memory in which each entry is as wide .... The **first** plain text cell is sent to **encryption** device module 52.1, ...

[www.wipo.int/pctdb/en/wo.jsp?IA=US1996005083&DISPLAY=DESC](http://www.wipo.int/pctdb/en/wo.jsp?IA=US1996005083&DISPLAY=DESC) - 65k

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#### 6. \*EP000582395B1\*

host **encryption/decryption** key established for each pair ... in the **packet** header 132 includes a **network** destination .... originating host computes a **packet authentication** code .... then temporarily stored in a **FIFO (first in first out) buffer** ... is valid, and a **buffer pointer** 236, which is the **buffer** ...

<https://publications.european-patent-office.org/PublicationServer/documentpdf.jsp?iDocId=5305830&iebug=.pdf> - [Similar pages](#)

#### 7. Scalable Security Processor Design and Its Implementation

During the cryptographic/**authentication** operations of the **network** applications such as IPSec and SSL, ... The **first** word is the descriptor header that defines the security operation. .... Data **FIFO**. Descriptor. **Buffer**. 16x32 bit. Crypto-Channel #1 .... file **encryption/decryption**, on-line video streaming and web ...

[ieeexplore.ieee.org/iel5/4017511/4017512/04017644.pdf](http://ieeexplore.ieee.org/iel5/4017511/4017512/04017644.pdf) - [Similar pages](#)

by CH Wang

#### 8. Method and processing interface for transferring data between host ...

6 illustrates a memory buffers diagram of a **first-in first-out (FIFO) buffer** .... plain text interface port 33 to **cipher** text interface port 37 or vice versa. .... Functions may include for example, **encrypt, decrypt, sign, authenticate**, etc. .... Inventor: DiChiara , et al.4872159, **Packet network** architecture for ...

[www.patenistorm.us/patenis/5961626/description.html](http://www.patenistorm.us/patenis/5961626/description.html) - [Similar pages](#)

9. [Intel IXP 2800/2850](#)

**Decryption, Authentication** (inbound). **Encryption, Authentication** ... Branch target **buffer** – 128 entries; provides history and prediction from insn cache .... Think of their job; grab a **packet**, act on it, move it on to the next ..... **Decryption** works by **decrypting** the **first cipher** text block, and then using ...

[www.cs.uiuc.edu/homes/luddy/PROCESSORS/IXP2850.pdf](http://www.cs.uiuc.edu/homes/luddy/PROCESSORS/IXP2850.pdf) - [Similar pages](#)

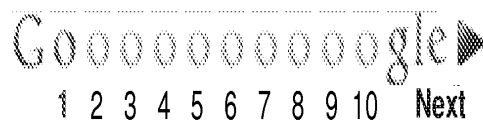
10. [PeerStreaming: design and implementation of an on-demand ...](#)

The timeblock data **pointer** points **out** the loca- .... relatively low-complexity algorithm to **encrypt/decrypt** the individual media **packet**. ... time of **decrypting** the **packet**, the **packet** key is **first** ..... playing **buffer** and may be shrunk to combat **network** ..... the received content to its disk in a **FIFO** fashion. Here, ...

[www.ingentaconnect.com/content/kiu/530/2007/00000013/00000003/00000073?](http://www.ingentaconnect.com/content/kiu/530/2007/00000013/00000003/00000073?crawler=true)

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